



500.40612X00

Applicant: Mitsugu KUSUNOKI
Serial No.: 09/944,134
Filed: September 4, 2001
For: SEMICONDUCTOR INTEGRATED CIRCUIT WITH
SUPPRESSED CLOCK SKEW
Group: 2631
Examiner: J. Torres

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

June 10, 2005

Sir:

In response to the Office Action dated January 10, 2005, the period for reply thereto being extended two months by the attached Petition for Extension of Time to expire June 10, 2005, reconsideration and withdrawal of the outstanding rejections and allowance of the present application are respectfully requested in view of the following amendments and remarks.

Amendments to the Specification and Abstract begin on page 2.

Amendments to the Claims begin on page 7.

Amendments to the Drawings describing changes to drawings shown in the Appendix are on page 12.

Remarks are included following the amendments.

An **Appendix** including amended drawing figures is attached following the Remarks.